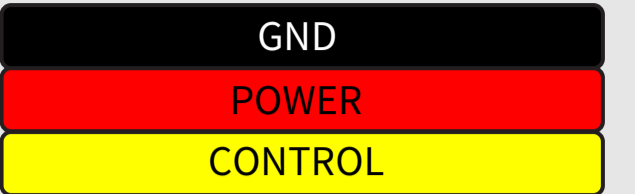
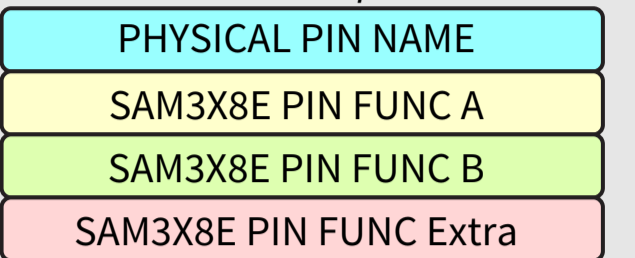


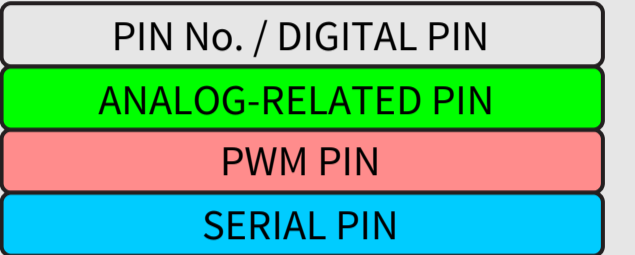
LEGEND



Atmel related qualifiers



Arduino related qualifiers



⊖ more Evaluation needed

⚡ High Current Pin

Source: -15mA

Sink: 9mA

⚡ Low Current Pin

Source: -3mA

Sink: 6mA

max total current: 130mA

⚠ see annotation for pin details

Version 0.9.7 β



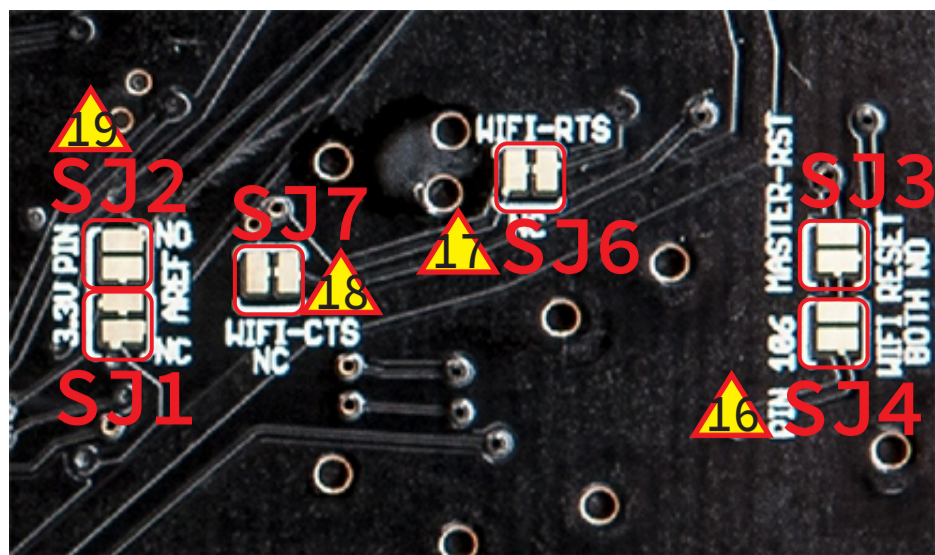
the unofficial PIN reference of

see: <http://digistump.com/board/index.php/topic,1211.0.html>

Usage on your own risk! This documentation may contain still severe errors. It is not meant as an replacement for other documentation!

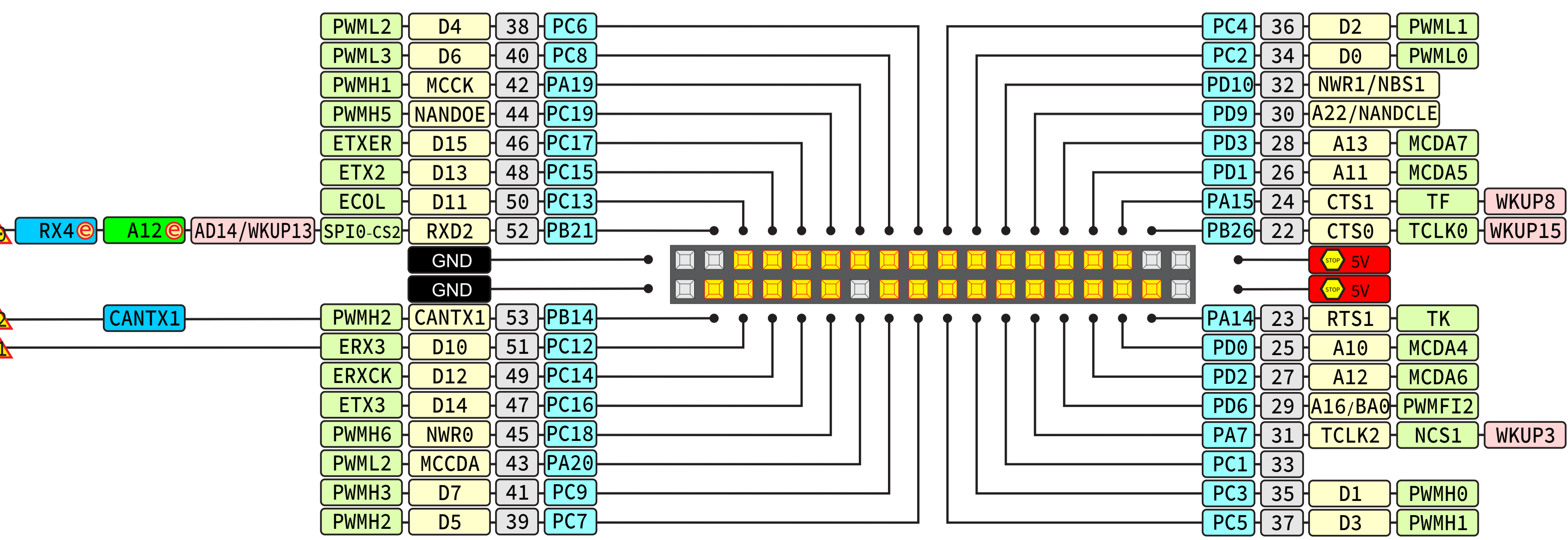
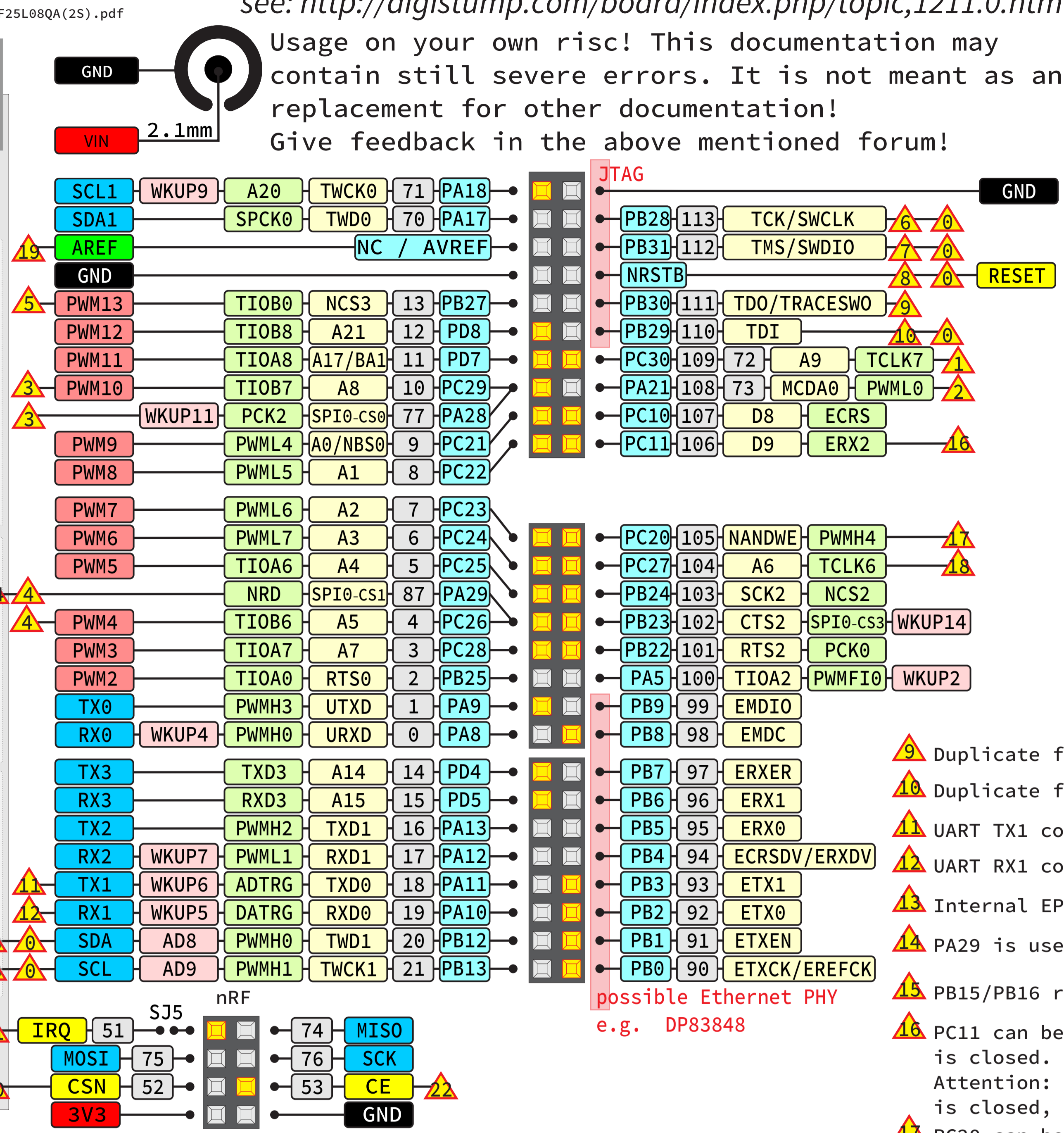
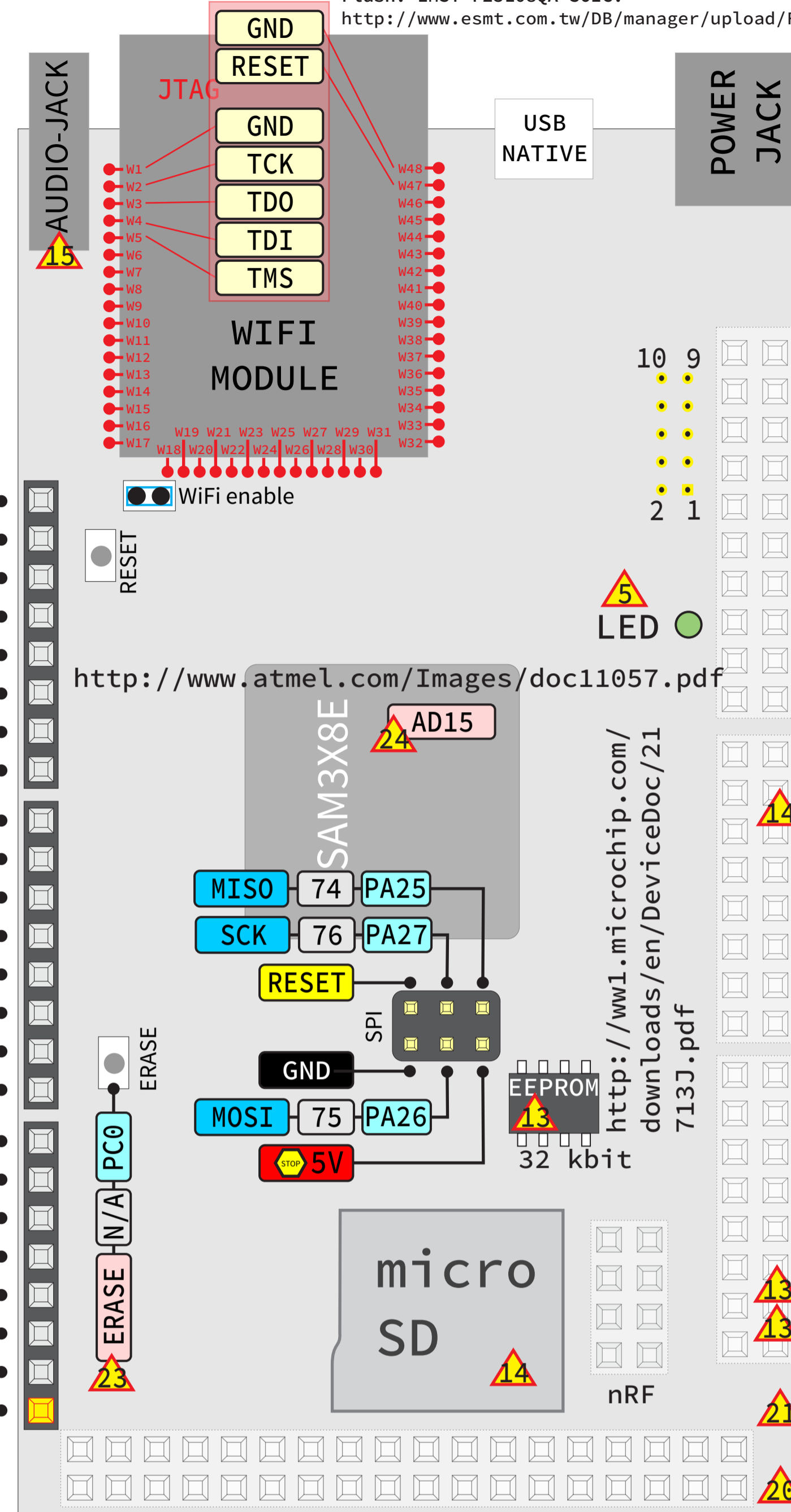
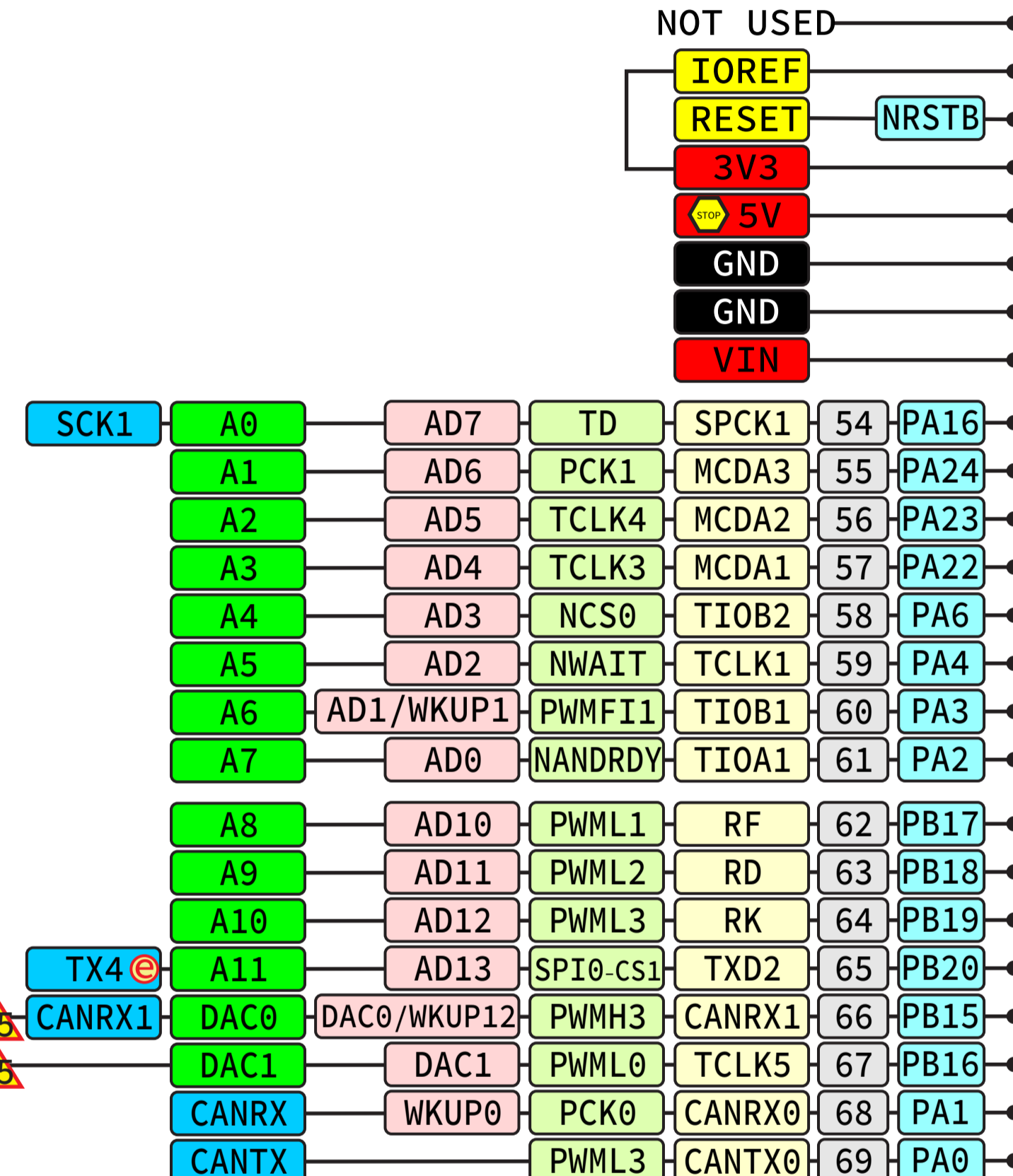
Give feedback in the above mentioned forum!

Solder Jumpers: Position, default setting, purpose



- SJ1 c/ AVREF to 3.3V
- SJ2 n/c AVREF to AREF-Pin
- SJ3 n/c Wifi Reset to Master Reset
- SJ4 n/c Wifi Reset to Pin 106
- SJ5 n/c nRF IRQ to PIN 51
- SJ6 c/ WiFi RTS to PIN 105
- SJ7 c/ WiFi CTS to Pin 104

WIFI MODULE:
http://digistump.com/wiki/_media/digix/tutorials/usr-wifi232-g_en.pdf
 MCU: Atmel ATSAM4S8B QFN:
<http://www.atmel.com/devices/SAM4S8B.aspx>
 Radio SoC: Mediatek MT5931SA BGA:
http://www.mediatek.com/_en/01_products/04_pro.php?sn=1052
 Flash: EMST F25L08QA S0IC:
[http://www.esmt.com.tw/DB/manager/upload/F25L08QA\(2S\).pdf](http://www.esmt.com.tw/DB/manager/upload/F25L08QA(2S).pdf)



- ⚠ 1k5 Pullup
- ⚠ PC30 is tied to logical PIN 72 for on-board RX-LED with negative logic on the DUE.
- ⚠ PA21 is tied to logical PIN 73 for on-board TX-LED with negative logic on the DUE.
- ⚠ PC29 and PA28 are crossconnected to one physical PIN on the board. The digiX ties PIN 109 for GPIO usage. that is to be backwards compatible to 8-bit Arduinos having PWM (TIO-port) and SPI-chip-select on one pin possible
- ⚠ PA29 and PC26 are crossconnected to one physical PIN on the board. Reason like above!
- ⚠ On-board LED via Q3 (transistor)
- ⚠ Duplicate from JTAG-Pin 4 (TCK)
- ⚠ Duplicate from JTAG-Pin 2 (TMS)
- ⚠ Duplicate from JTAG-Pin 10(RST)
- ⚠ Duplicate from JTAG-Pin 6 (TDO)
- ⚠ Duplicate from JTAG-Pin 9 (TDI)
- ⚠ UART TX1 connected to WiFi RX (WiFi-P39)
- ⚠ UART RX1 connected to WiFi TX (WiFi-P41)
- ⚠ Internal EPROM connected to I2C
- ⚠ PA29 is used as Chip-Select for SD-Card
- ⚠ PB15/PB16 right/left channel of AUDIO-Out
- ⚠ PC11 can be used to reset Wifi, if SJ4 is closed. SJ4 is open by default. Attention: SJ3 needs to be open, when SJ4 is closed, to prevent master-reset.
- ⚠ PC20 can be used for WiFi RTS, when SJ6 is closed. Open by default.
- ⚠ PC27 can be used for WiFi CTS, when SJ7 is closed. Open by default.
- ⚠ AREF is not connected by default. Closing SJ2 and opening SJ1 connects that pin to AVREF
- ⚠ PB21 is used as CHIP-select (SPI) for the nRF-subsystem. That makes this pin unusable as 4th U(S)ART or A12, when nRF is used!
- ⚠ PC12 can be used to react on the IRQ of the nRF-subsystem, when SJ5 is closed. Open by default!
- ⚠ PB14 is used vor Chip Enable (CE) of the nRF-subsystem.
- ⚠ PC0 can be used as GPIO, when ERASE is disabled.
- ⚠ AD15 is an internal temperature sensor. currently there is no PIN no. assigned.